Paper / Subject Code: 89341 / Electromagnetics and Antenna

TE/EXTC/SEM-VI/C-2019/DEC. 2023 [Time: 3 Hours] [Marks: 80]

N.B: Q.1 is compulsory.

Attempt any Three out of remaining questions
Assume suitable data.

		Assume suitable data.	6
Q.1	a)	Derive the integral form of continuity equation.	5
	b)	Explain concept of current density. How is magnetic field evaluated using	_5
		current sheet.	3
	c)	Describe Maxwell's equation for time varying fields.	5
	d)	Prove that $\overline{E} = -\overline{\nabla}v$. where Symbols have usual meanings.	5
Q.2	a)	Derive an expression for away of two isotropic sources with same amplitude and in phase currents.	10
	b)	Explain different methods of feeding of parabolic antenna.	10
Q.3	a)	Derive radiation resistance of infinitesimal dipole.	10
20	b)	Explain boundary conditions for electrostatic fields between two dielectric media.	10
Q.4	a)	State and explain principle of pattern multiplication. Explain concept of array factor.	10
	b)	Explain following concept of antennas with mathematical expressions. i) Radiation pattern ii) Directivity	10
Q.5	a)	Describe various configurations of horn antenna and explain H plane sectoral horn antenna.	10
, ja	b)	What is reactive near field. Discuss its importance in communication field and its applications.	10
Q.6	a)	Explain sky wave propagation with reference to D,E and F regions and multiple reflections.	10
	b)	Design a rectangular microstrip patch with dimension W and L, over single	10
	F	make the FD 4 3	

substrate FR4 whose center frequency is 2.45 GHZ. The height of substrate is 1.6 mm. Find the dimensions W and L taking into account the fringing field.

Paper / Subject Code: 89342 / Computer Communication Networks

TE | sem- VI | Extc | C-2019 | Dec- 2023

-	mie.	3 hours Max. Marks: 80	ĺ
1	V.B. :	(1) Question No.1 is compulsory	
		(2) Answer any three questions from Q. 2 to Q.6.	
		(3) Draw a neat diagram if necessary.	į,
1		Solve any four	=
	a	Draw OSI reference model. List the hardware devices, functions and protocols at each layer.	
		Compare IPv4 and IPv6	
		Explain Three-Way Handshaking for connection establishment in TCP	
	d	Compare Star, Mesh, and Bus Topology	
		An ISP grants a block of 195.2.6.0/24 addresses. It is required to have 14 subnets.	
		Determine the i) slash notation, ii) subnet mask, ii) first usable IP address and	
		broadcast address belonging to first subnet.	
		Explain the principles of circuit switching and packet switching. Compare circuit switching and packet switching	
	b	Compare twisted pair, coaxial and optical fibre cables. State at least one advantage and drawback of each cable.	
,	a	Explain the HDLC frame format with a neat diagram. Explain HDLC bit stuffing and destuffing.	
		Draw IPV4 Header and explain the meaning of various fields associated with it	
	a	Explain why IP uses ICMP? State ICMP message types and explain with neat diagrams.	
	b	Differentiate between TCP and UDP. The following is the dump of the UDP header in hexadecimal format. 0045DF0000500000	
		a. What is the source port number?	
		b. What is the destination port number?	
		C. Is the pecket directed from the client to the	
		c. Is the packet directed from the client to the server or vice versa? d. State which transport layer protocols would be necessary for each of the	
		d. State which transport layer protocols would be necessary for each of the following applications	
	9 1	DNS, DHCP, File Transfer, Web Browsing, and remote login	
	8	Explain pure ALOHA and slotted ALOHA with neat diagrams. Also compare pure and slotted ALOHA.	
	b 1	raw and explain TCP segment header. The following is a dump of a TCP header in nexadecimal format	1
		053200016 00000000F 00000000 700207FF 0000000	
	1) What is the source port number?	
	2	2) What is the destination port number?	
	3	3) What is sequence number?	
	4) What is the acknowledgement number?	
	5	What is the length of the header?	
	1	Write a short note on (Solve any 2)	2
	a I	DHCP	2
	b l	Jnicast routing protocols	
		DNS	

6. P.code

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Paper / Subject Code: 89343 / Image Processing and Machine Vision

Duration: 3 hrs [Max Marks: 80]

N.B.: (1) Question No 1 is Compulsory.

- (2) Attempt any three questions out of the remaining five.
- (3) All questions carry equal marks.
- (4) Assume suitable data, if required, and state it clearly.

	Attempt any FOUR.			[20]
a l	Explain Butterworth frequency domain filtering.		185	5
b 1	List edge models and explain any one in short.			5

c For the following 2 images perform 1) P1 = A*B and 2) P2= max(A, B) while Result = P1 - P2 which is a 4-bit unsigned image.

$$A = \begin{bmatrix} 2 & 3 & 3 \\ 3 & 4 & 18 \\ 4 & 3 & 8 \end{bmatrix} \qquad B = \begin{bmatrix} 2 & 5 & 8 \\ 3 & 2 & 3 \\ 5 & 2 & 1 \end{bmatrix}$$

- d True or False: Second order filter is better than first order filter in edge detection.

 5
 Justify.
- e With example explain Distance measures.
- 2 a Explain Canny edge detection algorithm in detail steps with proper diagrams. [10]
 - b Write brief notes on any two of the following [10]
 - 1. Thresholding
 - 2. Fundamental steps in image processing.
 - 3. High boost filtering
- 3 a Apply 3x3 averaging filter and median filter on following image. Use mirror [10] padding.

	1	4	0	1	3	1
	2	2	4	2	2	3
	1	0	1	0	1	0
7	1	2	1	0	2	2
	2	5	3	1	2	5
	1	1	4	2	3	0

p. code

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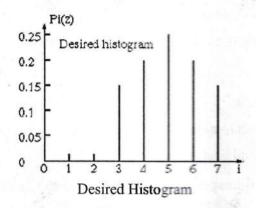
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Paper / Subject Code: 89343 / Image Processing and Machine Vision

A 3-bit 64x64 image with 8 intensities is described in following table. Perform [10] histogram equalization for it. The desired histogram is shown in figure below.

Level	0	1	2	3	4	5	6	7
Intensity	790	1024	850	656	329	245	122	81



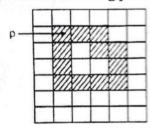
Explain Hit and Miss transform with example.

Elaborate on the following (5 marks each)

[10]

[10]

- 1. Log transformation
- 2. Co-occurrence Matrix
- Find the Chain code, Shape no., and first difference for the following image [10] using 4 connectivity. Arrow indicates starting point.



Generate Haar Basis for N=4.

[10]

- Describe the k-means algorithm with proper diagrams. Explain all the steps.
 - Explain region growing algorithm with proper example.

[10]

[10]

Paper / Subject Code: 89344 / Artificial Neural Network and Fuzzy Logic TE | 3em-VI EXTC | C-2019 Dec-2023 (3 Hours) **Total Marks: 80** N.B.: (1) Questions No.1 is compulsory. (2) Solve any three questions out of remaining (3) Draw neat labeled diagram whenever necessary (4) Assume suitable data if necessary Answer any four questions How do you define core boundary and support of a fuzzy set With neat diagram and necessary equations, List the different types activation functions. 05 What do you mean by K-Means algorithm? Where is it used? 05 If A and B are two fuzzy sets with membership functions: $\mu_a(\chi) = \{0.5, 0.2, 0.1, 0.7\}$ 05 $\mu_b(\gamma) = \{0.8, 0.3, 0.4, 0.1\}$, prove DE Morgan's theorem. An image of size 32 x 32 is applied to CNN architecture. Using a kernel of size 5 x 5 and with 05 a stride of 2, find out the size of output image after first CNN layer without padding. If this layer is followed with max pooling kernel of size 2 x 2 with stride 2, what is the resultant image size? With neat flowchart, explain the training steps and testing steps for Perceptron. 10 Briefly discuss the architecture of CNN. 10 Construct a Kohonen Self Organizing map to cluster given vectors [0 0 1 1], [1 0 0 0] and [1 10 101]. The number of clusters to be formed is 2. Consider the learning rate as 0.25. The weight matrix is given by $w_{ij} = \begin{vmatrix} 0.1 & 0.5 \\ 0.6 & 0.9 \\ 0.8 & 0.7 \\ 0.2 & 0.5 \end{vmatrix}$ 10 Implement OR gate using MP Neuron. Construct a discrete Hopfield network to store the patterns [1 1 -1 1], [-1 -1 1 -1] [1 -1 -1 -1], 10 [1111]. Calculate the energy of the stored patterns. Draw the architecture of Biological Neuron. Compare Artificial Neuron with Biological 10 Neuron. What are the different types of membership functions? Discuss with diagram. Give their 10 practical applications in fuzzy systems. Discuss the Support Vector algorithm in detail 10 What is defuzzification? What are the various methods of defuzzification? 10 Design a fuzzy controller to decide the speed of a train approaching a station.

b.

C.

10

Paper / Subject Code: 89346 / Computer Organization and Architecture (DLOC)

TE| Sem-VI/EXTC / C-2019 | Dec-2023

Duration Three Hours

Total marks 80

- 1. Question No 1 is compulsory and attempts any three out of remaining five questions.
 - 2. Assume suitable data wherever required.
 - 3. Figures to the right indicate full marks.

Solve any four-

- a) Explain Amdahl's law.
 b) Convert (-63.25)₁₀ in IEEE-754 single precision floating point representation.
 c) What's the difference between hardwired and microprogrammed control unit implementations
 d) What is a NUMA computer, and how is it used in a multiprocessor system?
 5
 e) Explain Memory Hierarchy.
- 2. a) Illustrate with example how throughput can be scaled with the number of cores

 10
 - b) List and briefly define the possible states that define an instruction execution.
- 3. a) What types of transfers must a computer's interconnection structure support?
 - b) Given x = 0101 and y = 1010 in two complement notation (i.e., x = 5, y = -6), compute 10 the product p = x * y with Booth's algorithm.
- 4. a) Explain the various levels of RAIDS.
 - b) Differentiate between Multiprocessor and Multicore systems. Also explain the structure/organization of each system.
- 5. a) Illustrate the concept of Virtual memory. How address translation is carried out?
 - b) In the realm of computer organization what is significance of Arithmetic mean,
 Harmonic mean, Geometric mean, Rate metric and Speed metric.
- 6. a) Differentiate between the fetch cycle and the execute cycle in the instruction cycle.

 Illustrate the sequence of micro-operations involved in each, highlighting their roles in processing instructions
 - b) List and explain the different microinstructions generated for the execution of an instruction ADD R1, [R2].

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